

XA-9898
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Kazuyuki TOKOROZUKI et al.

Appln. No.:

Filed: Herewith

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

* * *

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-
identified patent application as indicated below.

IN THE CLAIMS:

Please cancel Claims 4 and 5 without prejudice.

REMARKS

Claims 4 and 5 have been canceled to avoid the excess
claims fee.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

MWS:lat

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102-3833
(703) 903-9000

By: 

Mitchell W. Shapiro
Reg. No. 31,568

July 9, 2003